# ONEWAY PERMUTATIONS IN $N C^{0}$ 

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#### Abstract

We prove that there is a family of permutations which are computable by a family of $N C^{0}$ circuits while their inverses are P -hard to compute. Thus these permutations are oneway from the point of view of parallel computation.


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## 1. Permutations that are hard to invert.

A family of circuits $\left(C_{n}\right)_{n=1}^{\infty}$ is said to belong to $N C^{0}$, if for every $n, C_{n}$ has $n$ inputs and the depth of $C_{n}$ is bounded by a constant $K$ which is independent of $n$ and the fanin of any gate in the circuit is 2 . We will extend the notion slightly by having a family $\left(C_{i, n}\right)_{n=1}^{\infty}{ }_{i=1}^{n}$ where $C_{i, n}$ has $n$ inputs and the depth of any $C_{i, n}$ is bounded by $K$. Observe that the restriction implies that the size of any $C_{i, n}$ is bounded by $2^{K}$. We will in this paper investigate permutations from $\Sigma^{*}$ to $\Sigma^{*}$ which for each $n$ takes $\Sigma^{n}$ to $\Sigma^{n}$ and such that the $i$ th output bit is computed by $C_{i, n}$ for $N C^{0}$ family of circuits, and we call such a permutation a $N C^{0}$ permutation. We say that such a family of circuits is LOGSPACE uniform if there is a Turing machine $M$ which on input $1^{n}$ operates in $O(\log n)$ workspace and outputs the $n N C^{0}$ circuits which defines the permutation from $\Sigma^{n}$ to $\Sigma^{n}$.

Previous work has been done to determine the complexity of inverting permutations of the above type. Boppana and Lagarias [2] proved that there are permutations which were computable in $N C^{0}$ but whose inverses were as hard to compute as parity. These permutations can be said to be oneway since parity is known not to be computable even by unbounded fanin polynomial-size circuits [5]. Barrington [1] gave another example of a oneway function which was computable in $A C^{0}$, but computing its inverse was LOGSPACE-complete. We prove the following stronger result.

Theorem: There is a LOGSPACE-uniform family of $N C^{0}$ permutations which are $P$ complete to invert.

Proof: We will reduce the problem of evaluating a straight line program to the problem of inverting an $N C^{0}$ permutation. Since the former problem is well known to be P-complete [6] the latter will be P-hard. We will use the term P-complete to mean P-complete under LOGSPACE-reductions. Thus if a P-complete problem is in LOGSPACE every problem in P is in LOGSPACE. In a similar manner a problem is defined to be P -hard if it has the above property but is not known to be in P .

[^0]Let us set up some notation for the straight line program. The program contains a sequence of variables which are either defined as a part of the input or in terms of two previously defined variables. To make this formal let $i_{k}$ and $j_{k}$ be two indices which are less than $k$ and let $f_{k}$ be arbitrary functions of two Boolean inputs to one Boolean output.

Using this notation we define an instance of straight line program.
INPUT: Boolean variables $x_{1}, x_{2}, \ldots x_{n}$
PROGRAM: $x_{k}=f_{k}\left(x_{i_{k}}, x_{j_{k}}\right), \quad k=n+1, n+2, \ldots m$
OUTPUT: Value of $x_{m}$.
We will reduce this problem to the question of inverting an $N C^{0}$ permutation. Let us denote the permutation by $g$. It will be defined from $\{0,1\}^{m}$ to $\{0,1\}^{m}$ where $m$ is the number of variables occurring in the straight line program. Let $z_{1}, z_{2}, \ldots z_{m}$ denote the input bits and $g_{1}, g_{2} \ldots g_{m}$ the output bits. Let $\oplus$ be exclusive or.

Then,

$$
\begin{gathered}
g_{k}(z)=z_{k} \quad k=1,2, \ldots, n \\
g_{k}(z)=z_{k} \oplus f_{k}\left(z_{i_{k}}, z_{j_{k}}\right) \quad k=n+1, n+2, \ldots m
\end{gathered}
$$

where $i_{k}, j_{k}$ and the functions $f_{k}$ are the same as in the straight line program.
Let us establish that the reduction is correct.
Fact $1 g$ is a permutation.
We need only show that $g$ is onto. Given $y \in\{0,1\}^{m}$ find $z \in\{0,1\}^{m}$ such that $g(z)=y$ by solving for $z_{1}, z_{2}, \ldots z_{m}$ in increasing order. This can be done since the equations can be written $z_{k}=y_{k} \oplus f\left(z_{i_{k}}, z_{j_{k}}\right)$.
Fact 2 The $m$ 'th bit of $g^{-1}\left(x_{1}, x_{2} \ldots x_{n}, 0, \ldots, 0\right)$ is the output of the straight line program.
Solving for this input as described above performs the computation of the straight line program.
Fact 3 The reduction from straight line programs to permutations is effective and $g$ is computable by $N C^{0}$ circuits.

The reduction is trivial computationally since it just replaces equality signs by $\oplus$. The second part of Fact 3 follows from the fact that any function that only depends on a constant number of inputs can be computed in $N C^{0}$.

To prove the theorem we need to establish that there is a uniform family of straight line programs which are P-complete to evaluate. To see this let us recall the reduction from any problem to a straight line program.

Let $M$ be a Turing machine which solves a P-complete problem. The standard reduction to a straight line program uses the computation tableaux of $M$ on input $x$ which is a matrix ( $m_{i, j}^{x}$ ) where $m_{i, j}^{x}$ contains information about the the $i$ th square of $M$ 's tape at time step $j$. In particular $m_{i, j}^{x}$ contains the following information: the content of the square, whether the head is there and in this case which state the machine is in. Each $m_{i, j}^{x}$ will correspond to a constant number of variables of the straight line program using a suitable coding scheme. Now since $m_{i, j}^{x}$ only depends on $m_{i-1, j-1}^{x}, m_{i, j-1}^{x}$, and $m_{i+1, j-1}^{x}$ it is easy to make a short piece of straight line code which computes the variables corresponding to
$m_{i, j}^{x}$ from the variables corresponding to $m_{i-1, j-1}^{x}, m_{i, j-1}^{x}$, and $m_{i+1, j-1}^{x}$. The code for this computation is identical for all $i$ and $j$ and hence the only thing the machine constructing the straight line program has to remember is the index of the variables and this can be done in space $O(\log n)$. The input $x$ enters as bits specifying $m_{i, 0}^{x}$. This consludes the proof of the teorem.

We know ([3], [4], [7], [9]) that in the sequential setting the existence of oneway functions implies the existence of good cryptosystems.

There are two obvious obstacles to using the present results to construct parallel cryptosystems.

The first problem is that the function needs to be hard to invert on a random input. This is not quite achieved since even if we start with a straight line program which is hard to compute for a random input it is not necessarily true that the corresponding $N C^{0}$ permutation is hard to invert for random outputs. This is so as our reduction only maps to values of the permutation whose last $m-n$ bits are 0 .

The second problem is that the reductions from oneway functions to cryptographic generators is sequential i.e. even if the oneway function is easy to compute in parallel the resulting cryptographic generator will require large parallel time. For a discussion of "parallel cryptography" we refer to Reif and Tygar [8].

We have proved that there is a sequence of uniform $N C^{0}$ circuits which are P-complete to invert. An interesting open question is whether inverting every uniform family of $N C^{0}$ permutations is in P .

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## 2. References

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