

WHAT WE DID LAST LECTURE

- o TQBF PSPACE-complete
- o $PSPACE = NPSPACE$
- o Can simulate nondeterministic computation with quadratic blow-up in space
- o Logarithmic space: L and NL
- o $PATH = \{ \langle G, s, t \rangle \mid \exists \text{ path } s \rightarrow t \text{ in digraph } G \}$
 NL -complete
- o Some care needed with logarithmic space:
 - Reductions computed bit by bit
 - Witnesses ^{only} read-once in verifier-style definition
- o $NL = co-NL$
- ... And somewhere here we ran out of ^{time} space ...

Reduction must not be stronger than complexity class reduced to.

Prove $NL = coNL$ by showing
 $\overline{PATH} \in NL$

IS I

Construct reachance certificate (or show how
NL-machine can guess successfully

Yes-instance

$\langle G, s, t \rangle$ $s \rightsquigarrow t$ $n = |V(G)|$

Reaching

$R(i) = \{ \text{vertices reachable from } s \text{ in } \leq i \text{ steps} \}$

$s \rightsquigarrow t \iff t \notin R(\infty) \iff t \notin R(n)$

Idea

Compute $R(0) = \{s\}, R(1), R(2), \dots, R(n-1), R(n)$

Show $t \notin R(n)$

Problem

We cannot remember ^{em} $R(i)$ in log space

Only $|R(i)|$

Solution

Amazingly, this is enough!

Three subcertificates (that will be combined) IS II

$\boxed{\text{IS MEMBER}(v, i)} = "v \in R(i)"$
Just list path of length $i' \leq i$

$\boxed{\text{MEMBERSHIP EXPANSION}(i, r, r')} = "|R(i-1)| = r' \Rightarrow |R(i)| = r"$

$\boxed{\text{LIST MEMBERS}(i, r)} = \text{List of } r \text{ elements in } R(i)$
in increasing order, each with
IS MEMBER certificate

Full certificate:

MEMBERSHIP EXPANSION $(1, r_1, 1)$
MEMBERSHIP EXPANSION $(2, r_2, r_1)$
MEMBERSHIP EXPANSION $(3, r_3, r_2)$
 $\vdots$
MEMBERSHIP EXPANSION (n, r_n, r_{n-1})
LIST MEMBERS (n, r_n)

Verification

Check that r_i is correct, keeping r_{i-1} in memory
($\log n$ space for counters) for $i=1, 2, \dots, n$

Finally check that t is not listed
in LIST MEMBERS (n, r_n)

Done!

ISMEMBER and LISTMEMBER are clear.

IS III

MEMBERSHIP EXPANSION (i, r, r')

We already know $|R(i-1)| = r'$ (by assumption)

Give subcertificates for all vertices $j = 1, 2, \dots, n$
in increasing order

(a) $j \in R(i)$

$j: \text{ISMEMBER}(j, i)$

proves this
increment r by one.

(b) $j \notin R(i)$

$j: \text{LISTMEMBERS}(i-1, r')$

Go over list

For every member u , check

(i) $u \neq j$

(ii) u does not have edge to j

After having verified all subcertificates,
we know $r = |R(i)|$.

But note that for every single $j \in R(i)$,
the same long certificate $\text{LISTMEMBERS}(i-1, r')$
is repeated over and over again...
Extremely wasteful.

Summing up:

$$L \subseteq NL \subseteq P \subseteq NP \subseteq PSPACE \subseteq EXP$$

Some inclusions must be strict

[since $L \subsetneq PSPACE$ (space hierarchy theorem)
 $P \subsetneq EXP$ (time hierarchy theorem)]

But we don't know which

Probably most of them, or even all...

POLYNOMIAL HIERARCHY (AGAIN)

PH I

What lies between P and PSPACE?

C circuit, α assignment

$$\text{CIRCUIT EVAL} = \{ \langle C, \alpha \rangle \mid C(\alpha) = 1 \}$$

in P (in fact, P-complete).

$$\text{CIRCUIT SAT} = \{ C \mid \exists \alpha C(\alpha) = 1 \}$$

NP -complete

$$\text{MIN SIZE CIRCUIT} = \{ \langle C, s \rangle \mid \exists \text{ circuit } C' \text{ of size } \leq s \text{ s.t. } C' \equiv C \}$$

Two quantifiers:

$$\exists \text{ circuit } C' \quad \forall \text{ assignments } \alpha \quad C'(\alpha) = C(\alpha)$$

In other class Σ_2^P

$L \in \Sigma_2^P$ if $\exists$ poly-time deterministic TM M
 $\exists$ polynomial q
s.t. for binary strings $u_1, u_2 \in \{0,1\}^{q(|x|)}$
it holds

$$x \in L \Leftrightarrow \exists u_1, \forall u_2 \quad M(x, u_1, u_2) = 1$$

More generally:

DEF POLYNOMIAL HIERARCHY

Fix $i \in \mathbb{N}^+$

$L \subseteq \{0,1\}^*$ is in Σ_i^P if

$\exists$ deterministic poly-time TM M
 $\exists$ polynomial q

$$x \in L \Leftrightarrow \exists u_1 \forall u_2 \exists u_3 \dots Q_i u_i, M(x, u_1, u_2, \dots, u_i) = 1$$

where all $u_i \in \{0,1\}^{q(|x|)}$

$Q_i = \exists$ for i odd, $Q_i = \forall$ for i even

$$PH = \bigcup_{i \in \mathbb{N}^+} \Sigma_i^P$$

$$\Pi_i^P = \text{co}\Sigma_i^P = \{L : L \in \Sigma_i^P\}$$

$$\Sigma_1^P = NP \quad \Pi_1^P = \text{co}NP$$

$$\Sigma_i^P \subseteq \Pi_{i+1}^P \subseteq \Sigma_{i+2}^P$$

$$\text{so } PH = \bigcup_{i \in \mathbb{N}^+} \Pi_i^P$$

Is it true that

$$\Sigma_1^P \subsetneq \Sigma_2^P \subsetneq \Sigma_3^P \subsetneq \Sigma_4^P \subsetneq \dots ?$$

Is it true that "the polynomial hierarchy doesn't collapse" ?

Don't know, but widely believed
Standard assumption in complexity theory

THM

1. For every $i \in \mathbb{N}^+$ it holds that if $\Sigma_i^P = \Pi_i^P$, then $PH = \Sigma_i^P$ ("the polynomial hierarchy collapses to the i th level").
2. If $P = NP$, then $PH = P$ ("the polynomial hierarchy collapses to P ")

Many complexity theory results have form:

Unless $\langle$ statement we believe to be true $\rangle$ holds,
PH collapses to the i th level

Smaller $i \Rightarrow$ stronger result

Ex NP has poly-size circuits $\Rightarrow$ PH collapses to 2nd level
(so we don't believe $NP \subseteq P/poly$)

Proof

1. Was on pset 1

2. Prove by induction:

If $P = NP$, then $\sum_{i=1}^P P = \prod_{i=1}^P P = P$

Base case ($i=1$): Nothing to prove

By assumption $P = NP$

$\text{co}NP = \text{co}P = P$ (P closed under complement)

Induction step Suppose $\sum_{i=1}^P P = P = \prod_{i=1}^P P$

By definition $\prod_{i=1}^P P \subseteq \sum_{i=1}^P P$ so $P \subseteq \sum_{i=1}^P P$

Enough to prove $\sum_{i=1}^P P \subseteq P$. Then $P = \sum_{i=1}^P P$ and we can take complements to get $P = \prod_{i=1}^P P$.

Consider $L \in \sum_{i=1}^P P$. Want to show $L \in P$

By def, $\exists$ ^{poly-time} TM M and poly q such that

$x \in L \Leftrightarrow \exists u_1 \forall u_2 \dots Q_i u_i; M(x, u_1, \dots, u_i) = 1$

for $u_i \in \{0,1\}^{q(1 \times i)}$

Define L' by

$(x, u_1) \in L' \Leftrightarrow \forall u_2 \exists u_3 \dots Q_i u_i; M(x, u_1, u_2, \dots, u_i)$

By syntactic pattern matching $L' \in \prod_{i=1}^P P$

By inductive hypothesis $\prod_{i=1}^P P = P$

i.e., $\exists$ poly-time TM M' deciding L'

Then is,

$$\{(x, u_1) \in L' \Leftrightarrow M'(x, u_1) = 1$$

But then

$$x \in L \Leftrightarrow \exists u_1 \quad M'(x, u_1) = 1$$

so $L \in NP$

By induction hypothesis, $L \in NP = P$.

Since $L \in \Sigma_1^P$ was arbitrary, $\Sigma_1^P \subseteq P$, QED $\square$

DEF Language $L \subseteq \{0, 1\}^*$ is Σ_i^P -complete if

$$\bullet L \in \Sigma_i^P$$

$$\bullet \forall L' \in \Sigma_i^P \text{ it holds that } L' \leq_p L$$

Π_i^P -complete languages and
PH-complete languages defined analogously.

But: We believe PH is a class without
complete languages

LEMMA PH does not have complete
languages unless the hierarchy collapses.

Proof Suppose $\exists$ PH-complete language L .

$$PH = \bigcup_{i \in \mathbb{N}} \Sigma_i^P, \text{ so } \exists i^* \text{ s.t. } L \in \Sigma_{i^*}^P$$

But then every language in PH can
be reduced to $L \in \Sigma_{i^*}^P$ $\square$

PH VI

COROLLARY $PH \subseteq PSPACE$ but $PH \neq PSPACE$
unless the polynomial hierarchy collapses.

Proof If $L \in PH$, then there exists a
poly-time TM M s.t. $x \in L$ iff
 $\exists u_1 \forall u_2 \exists u_3 \dots Q_i u_i M(x, u_1, u_2, \dots, u_i)$

Do CIRCUITSAT reduction for M
Obtain QBF. Verifiable in PSPACE
(Or argue from first principles)

PSPACE has complete problems (TQBF,
for instance). So if $PSPACE = PH$, PH has
complete problems and the hierarchy collapses.

Complete problems for Σ_i^P

$\Sigma_1^P \text{ SAT} = \{ \psi \mid \psi = \exists u_1 \forall u_2 \exists u_3 \dots Q_i u_i \varphi(u_1, \dots, u_i) \}$
where φ propositional formula

For $\Sigma_{2i+1}^P \text{ SAT}$ can let φ be CNF formula.

For $\Sigma_{2i}^P \text{ SAT}$ not (why? Good exercise.)

$\Pi_i \text{ SAT}$ defined similarly

(and φ can be CNF for i even)

ALTERNATING TURING MACHINES

PH VII

A NDTM M is "on its way" to complete an accepting computation if $\exists$ path from current configuration to accepting configuration.

Such a machine can solve SAT

We could also imagine a "co-NDTM" M' that accepts if all paths from current configuration accept — would solve UNSAT

For MIN SIZE CIRCUIT, could use TM that combines both approaches:

- Start in "existential mode" to guess small circuit
- Then switch to "universal mode" to check circuit equivalence on all assignments

Such a machine could decide Σ_1^P -languages.

DEF (Informal; check Arora-Bank for full details)

An alternating TM has two transition functions and every state labelled $\exists$ or $\forall$.

- Accepting computation from $\exists$ -state if exists one path to accepting state
- Accepting computation from $\forall$ -state if all paths lead to accepting states.

L is in $ATIME(T(n))$ if decided by alternating TM in time $O(T(n))$

Alternation change along computation

path from $\exists$ - to $\forall$ - or from $\forall$ - to $\exists$ -labelled states

DEF 15.1/2 (Also a bit informal)

Σ_i TIME($T(n)$) : languages L decided by ATM that

- runs in time $O(T(n))$
- decides the language L
- start state labelled $\exists$
- along all computation paths, at most $i-1$ alternations.

Π_i TIME($T(n)$).

The same, except starting state labelled $\forall$

LEMMA 16

$$\Sigma_i^P = U_{CEIN} + \Sigma_i \text{TIME}(n^c)$$

$$\Pi_i^P = U_{CEIN} + \Pi_i \text{TIME}(n^c)$$

$$\underline{\text{PSPACE}} = \text{AP} = U_{CEIN} + \text{ATIME}(n^c)$$

What on earth is this good for?!

Can be used to show e.g. that SAT cannot be solved by machine running in nearly linear time and using much less than linear space

(But we'll skip this...)

PH IX

Yet another definition of PH (the third!)
via... oracle TMs

Recall

$M^O(x)$

Output of TM M on input x when
 M can ask "oracle queries" $y \in O$?
(and get answers in one time-step)

Fix cplx class $\mathcal{C}$

and (other) cplx class $\mathcal{D}$ possessing complete problems
(or same)

$\mathcal{C}^{\mathcal{D}} = \{ \text{cplx class } \mathcal{C} \text{ with addition of any complete language in } \mathcal{D} \text{ as oracle} \}$

THEOREM 17

For every $i \geq 2$ it holds that

$$\Sigma_i^P = NP^{\Sigma_{i-1}^P}$$

that is, ^{any} $L \in \Sigma_i^P$ can be decided by
an DTM M that runs in poly time,
makes nondeterministic choices,
and asks oracle questions about the
satisfiability of Σ_{i-1} SAT-formulas.

Not entirely trivial — see Arora-Barak
for details.

SUMMING UP

- o POLYNOMIAL HIERARCHY: Languages verifiable by "certificates" like

There is a subitness u_1

s.t. for all challenges u_2

there is a subitness $u_3 \dots$

$\dots$ such that TM accepts $\langle x, u_1, \dots, u_i \rangle$

- o PH is (strictly?) between P and PSPACE
- o PH can also be defined by
 - alternating TMs
 - oracle TMs
- o Doesn't correspond to any computational device we know of, but can be useful:
 - as technical tool to study concrete questions (state of the art for lower bounds that can be proven on SAT algorithms)
 - classify problems harder than NP (e.g. is an NP-solution "optimal")